

FIG. 1
(Prior Art)

FIG. 2
(Prior Art)

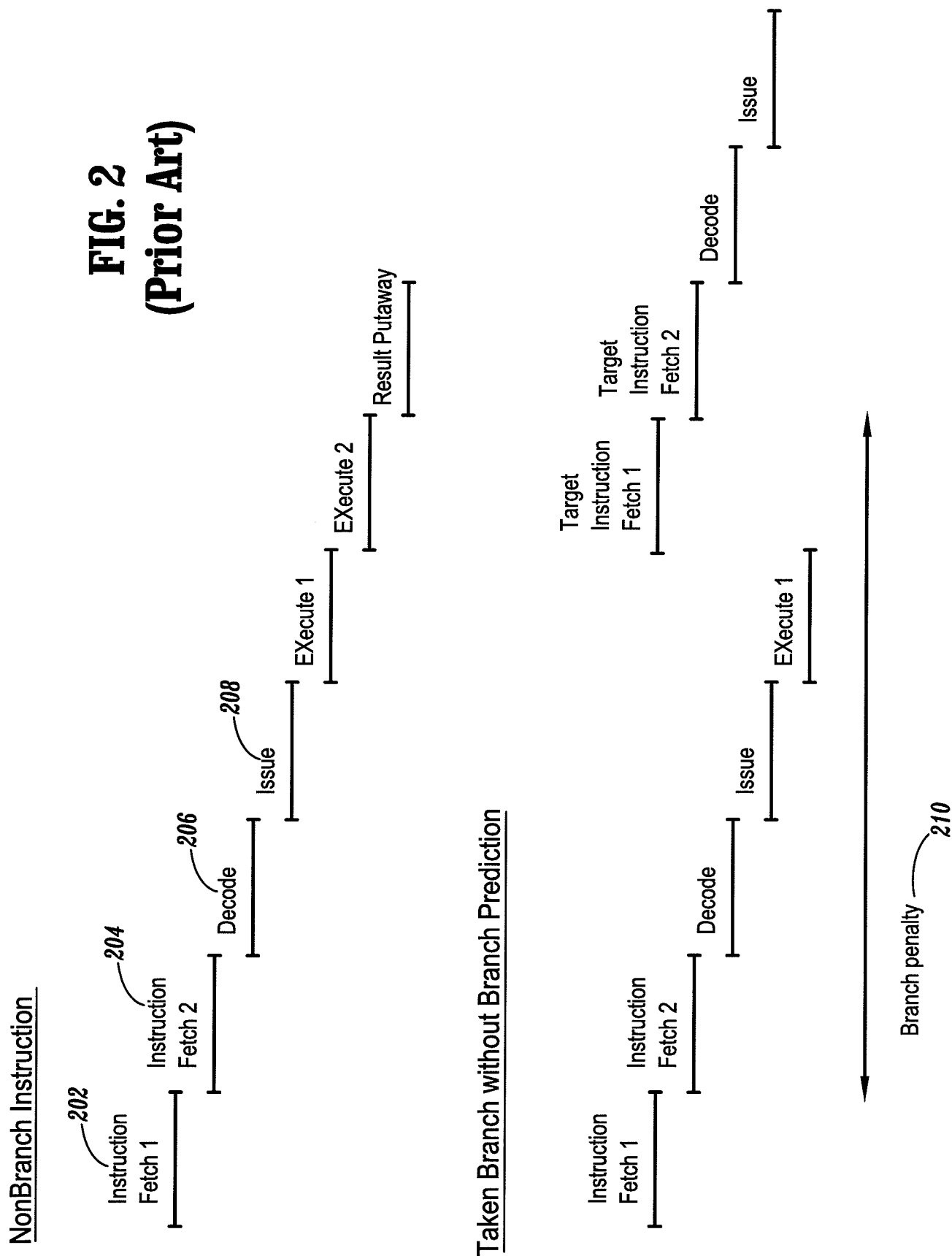
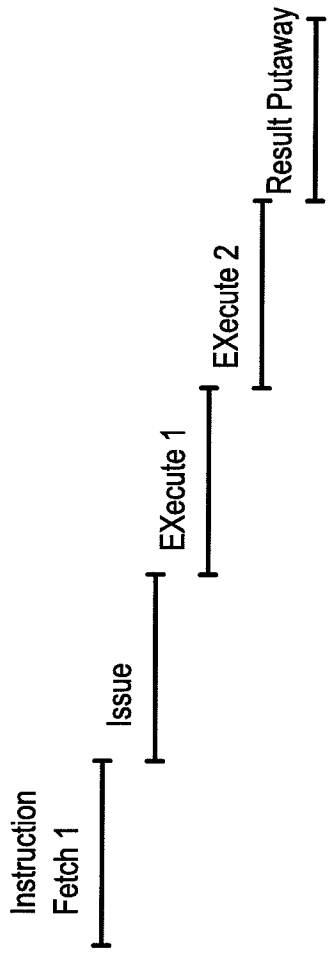


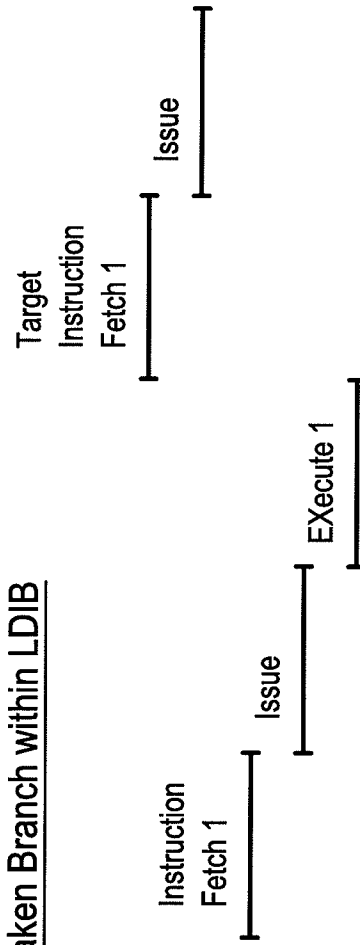


FIG. 3

NonBranch Instruction within Local Decoded Instruction Buffer



Taken Branch within LDIB



← Branch penalty →

FIG. 4